

F19. 1

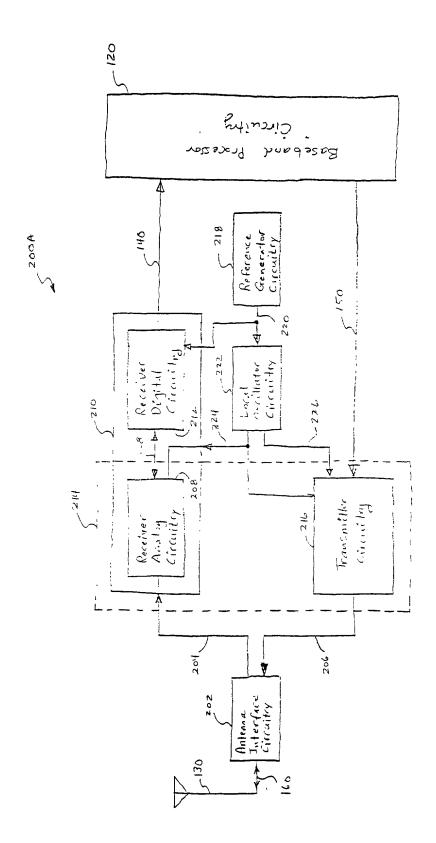
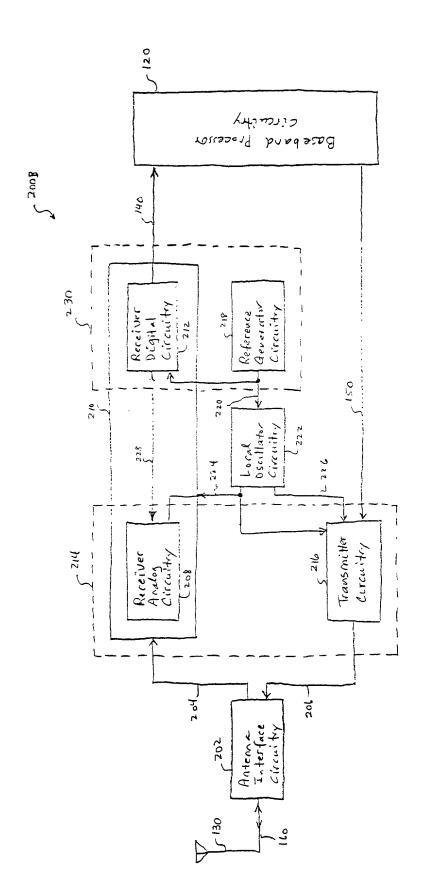


FIG. 2A



F16. 2B

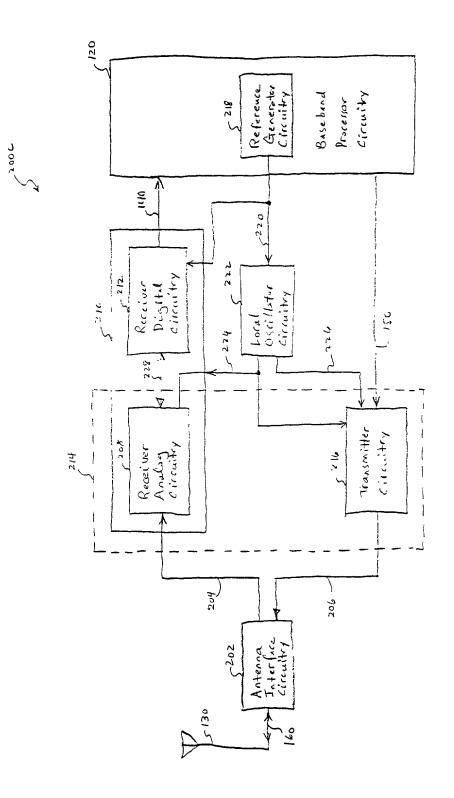
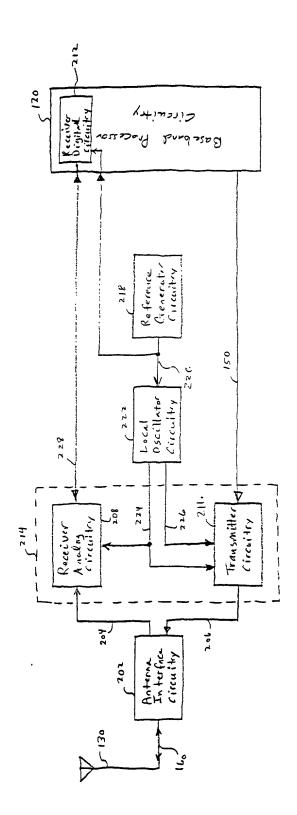
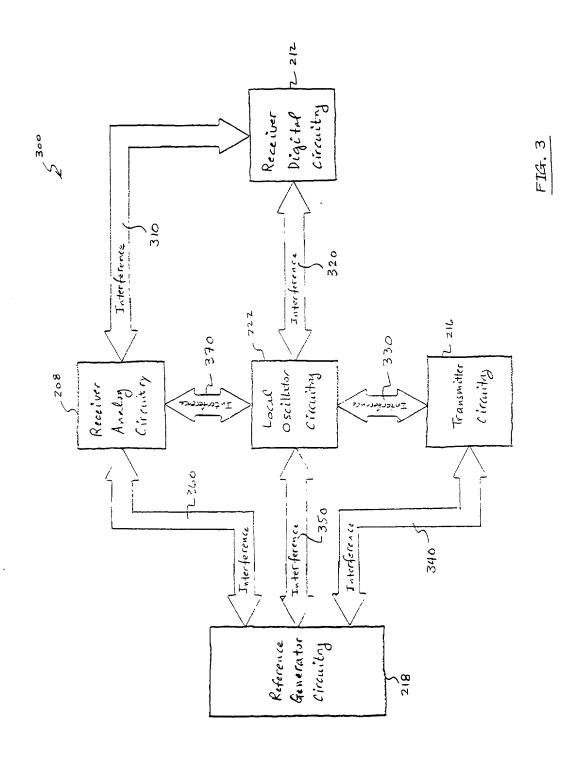


FIG. 2C

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F[6, 2D



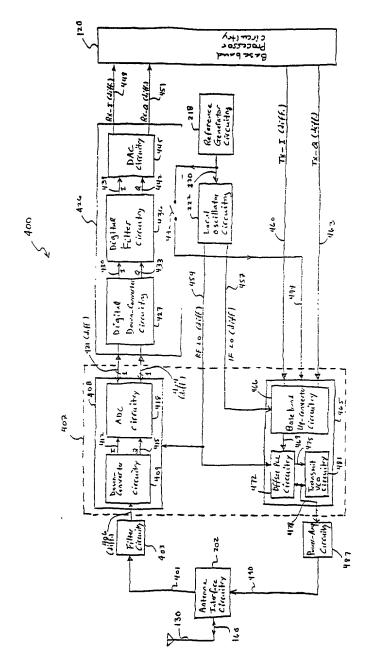
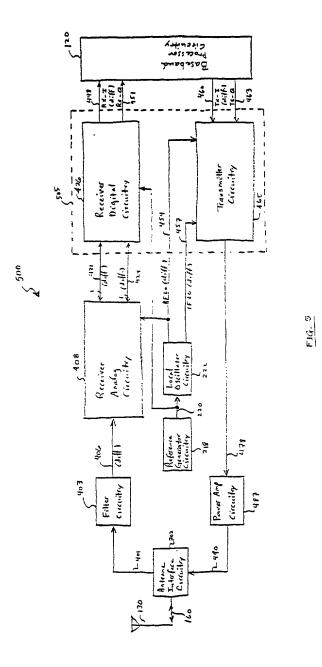


FIG. 4



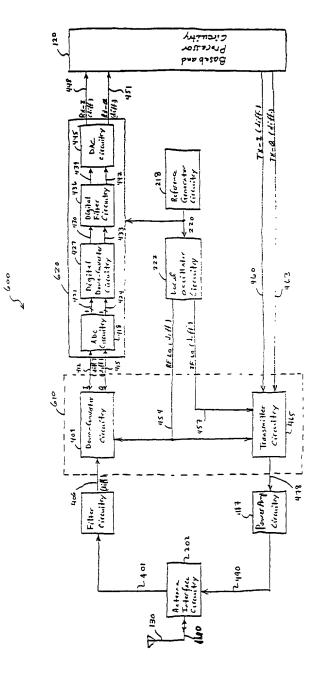
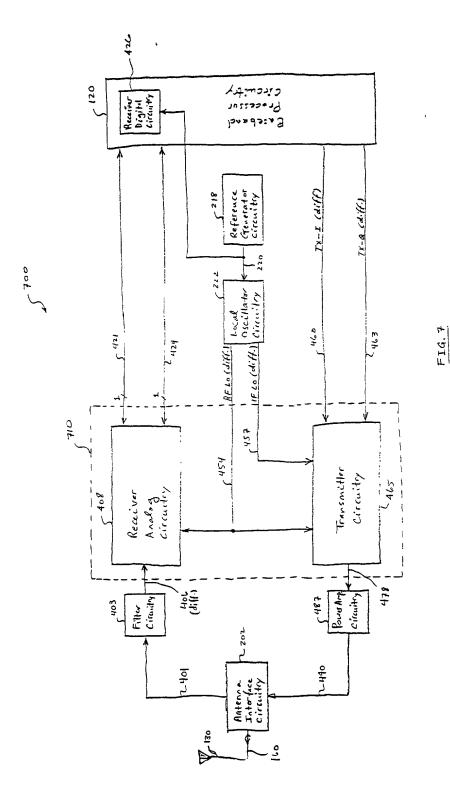
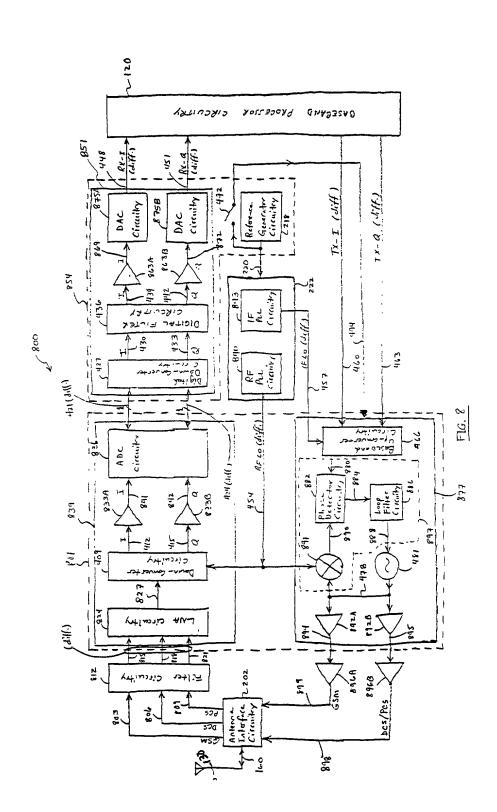


FIG. 6





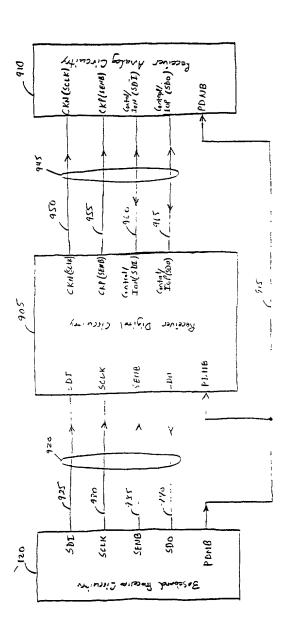


FIG. M

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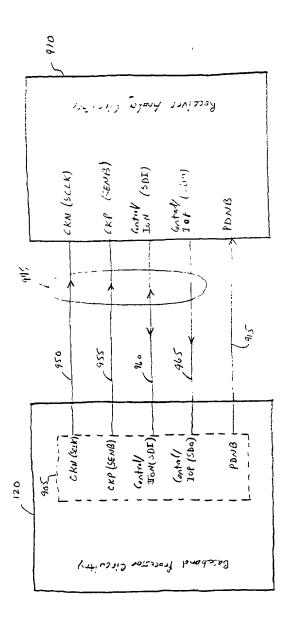
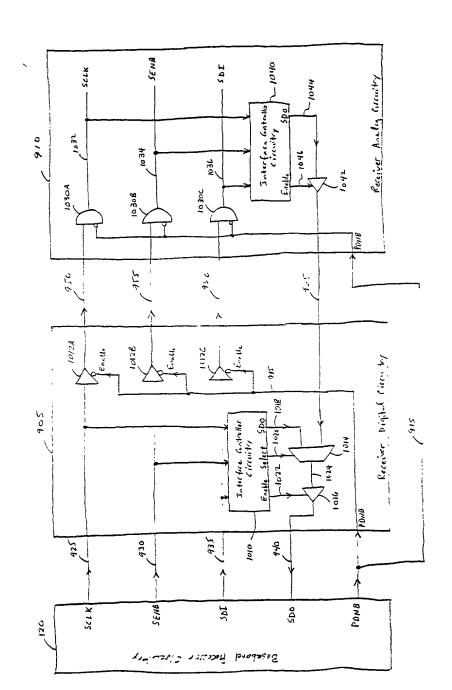


FIG. 98



FTG. 10

Vc011

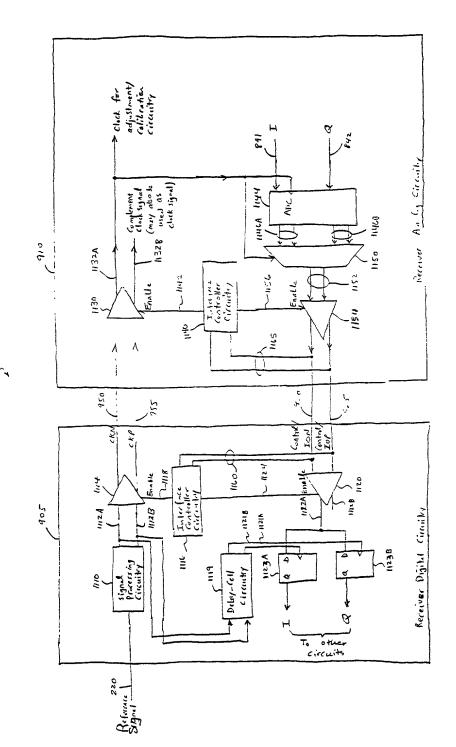
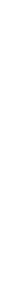
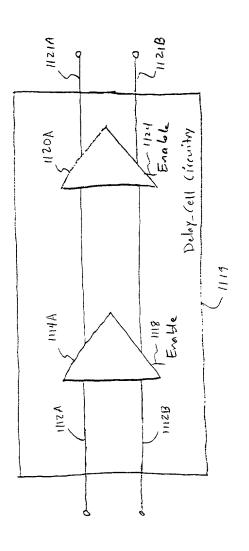


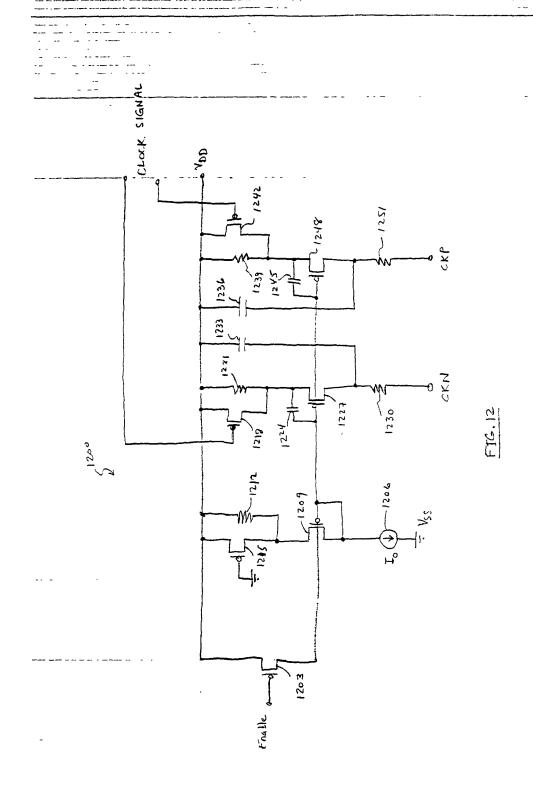
FIG. 11A

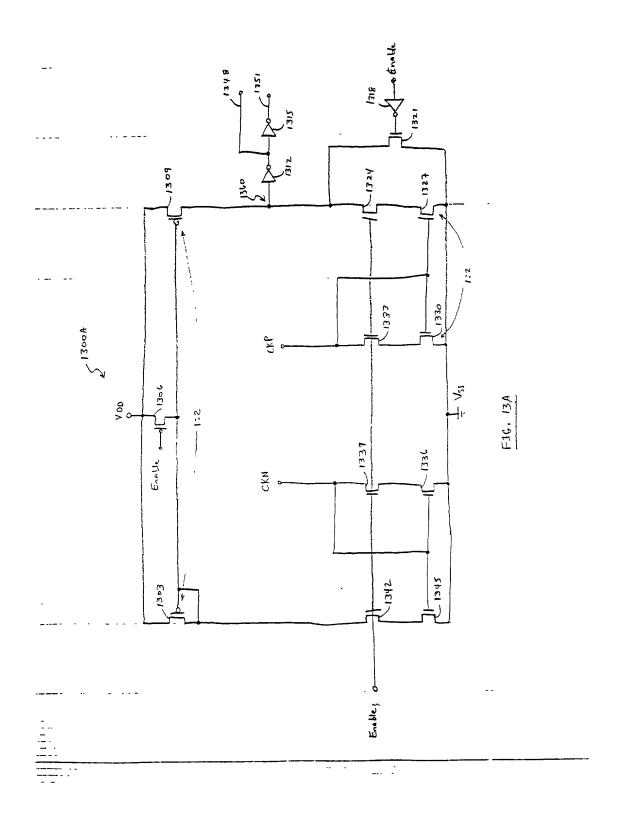


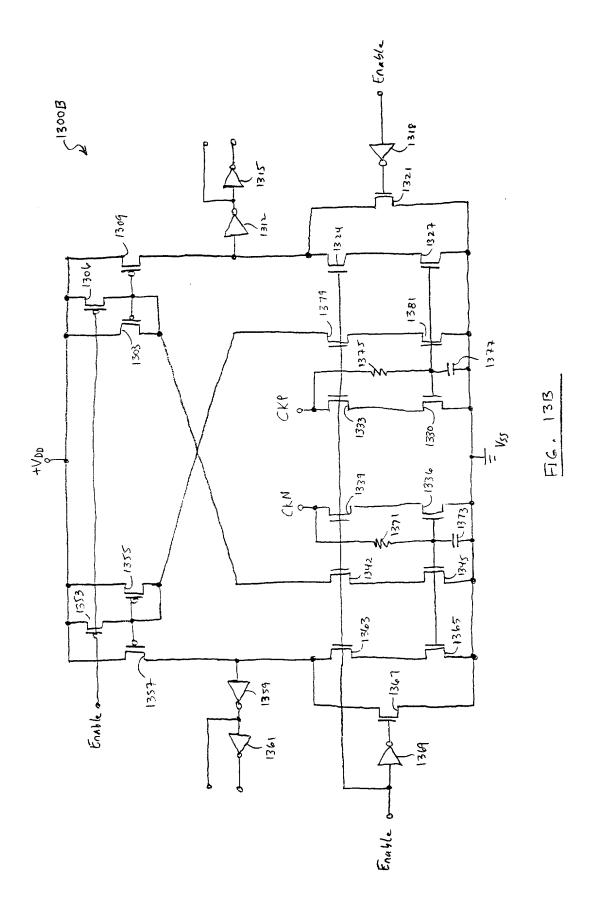
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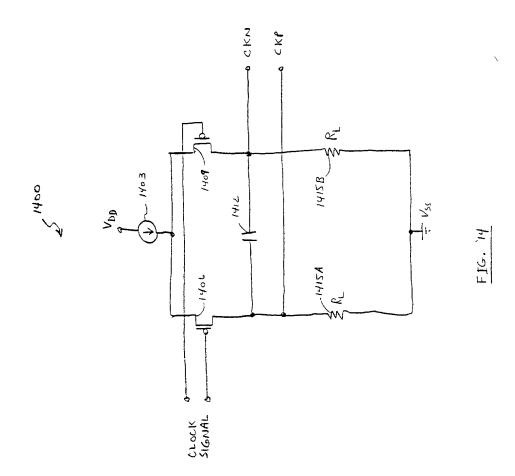
F16. 11B

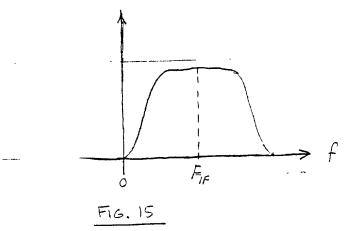


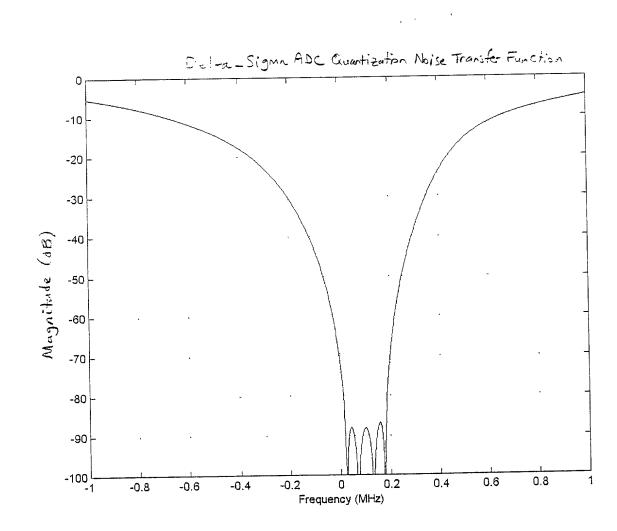








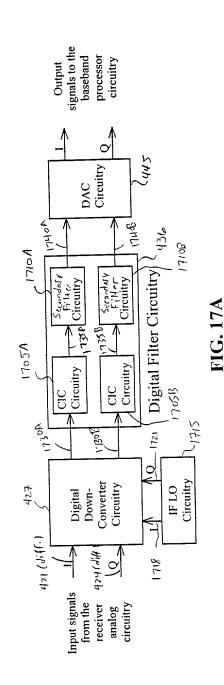




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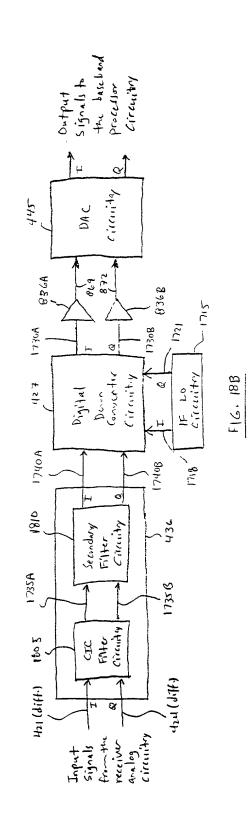
F16.16

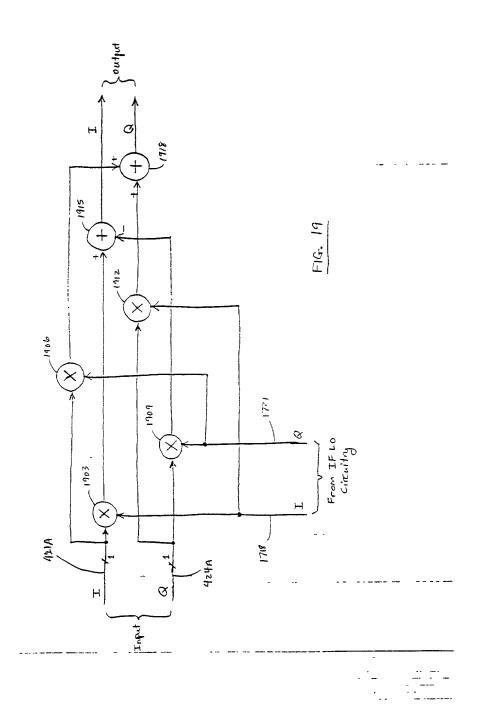


signals to the processor circuitry baseband Output o 1 DAC Circuitry 45 8-15 8¢,9 836A B36B 1767 1 HohLi gora A01 L1 17108 Secondury F-:Her Circuitry 18 1/2 1/2 1/2 Post Digital Filter Circuitry . :-CIC CIrcuitry Circuitry CIC1755 F 7 1715 1730 8 424) IF LO Circuitry Digital Down-Converter Circuitry 421 (diff.) 0 1718 Input signals from the circuitry receiver analog

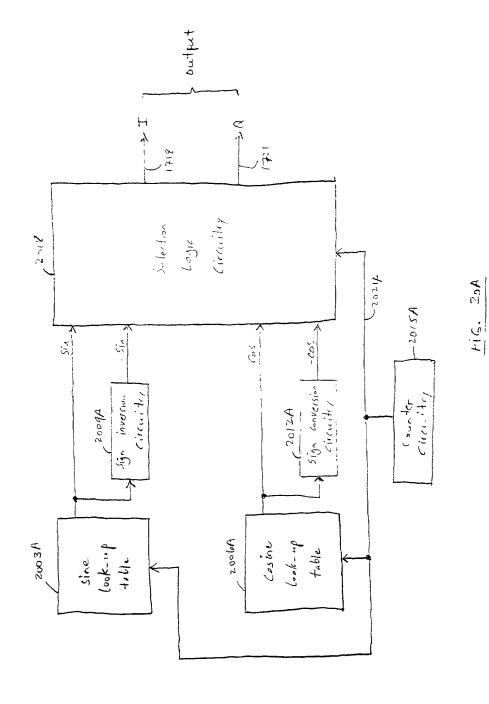
FIG. 17B

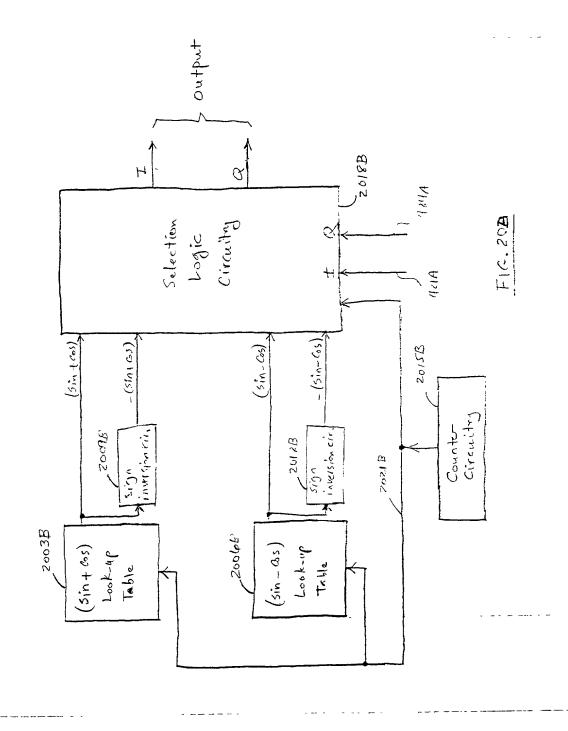
FIG. 18A

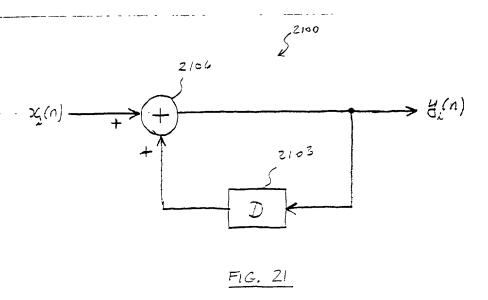


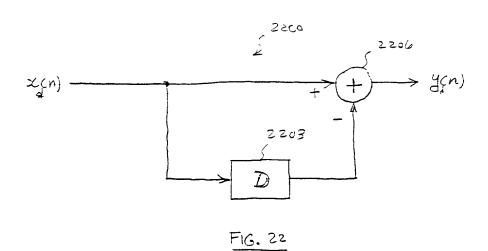


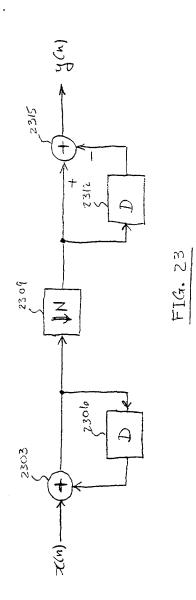
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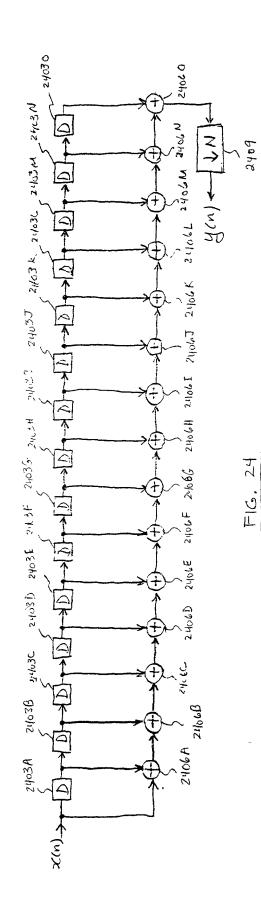


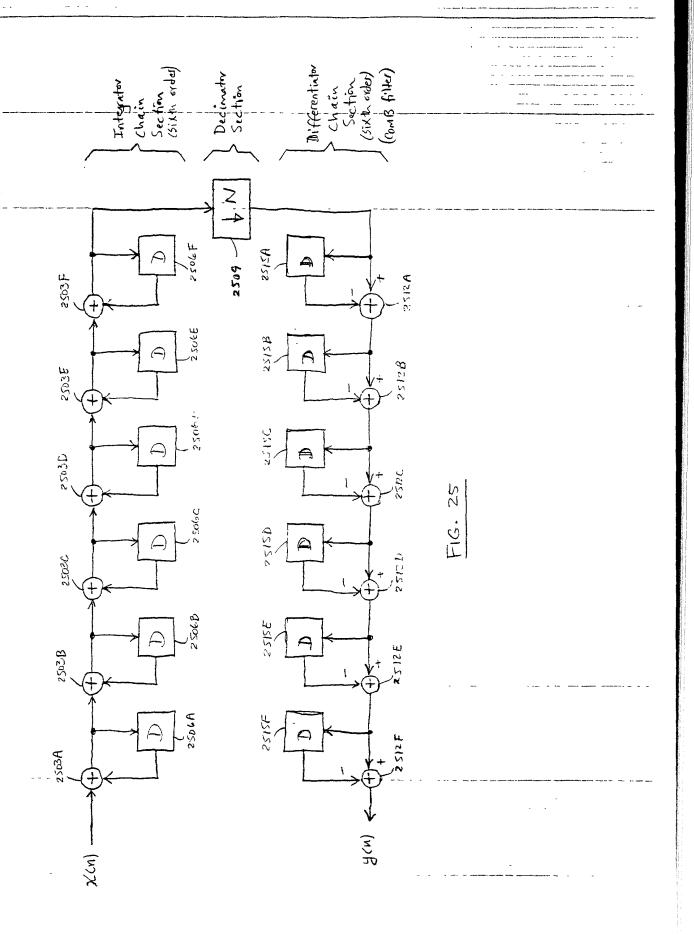












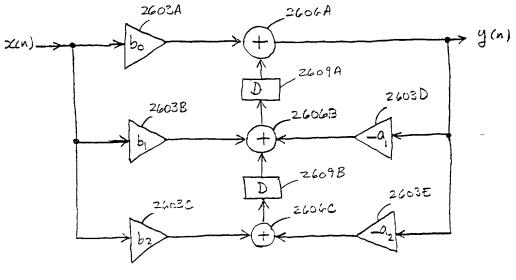


FIG. 26

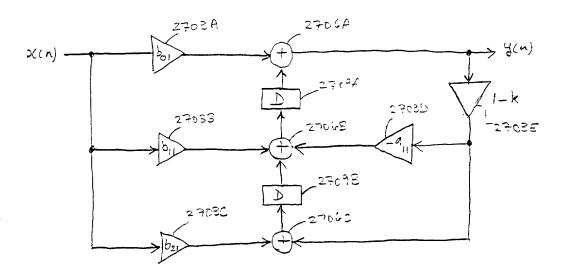
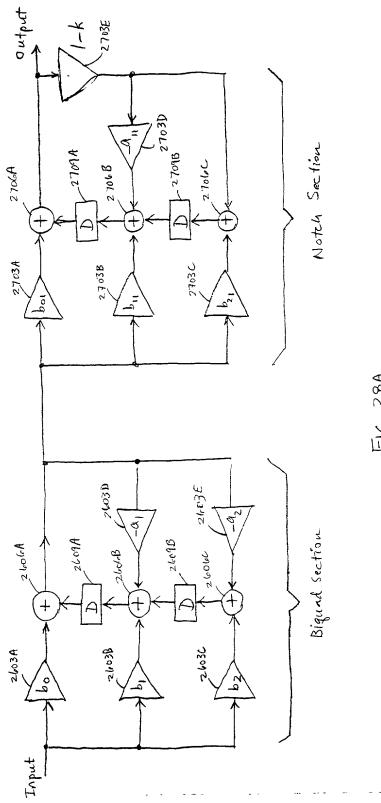
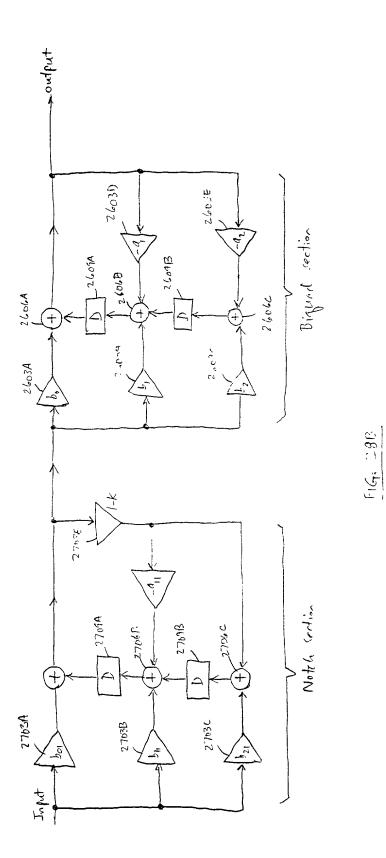
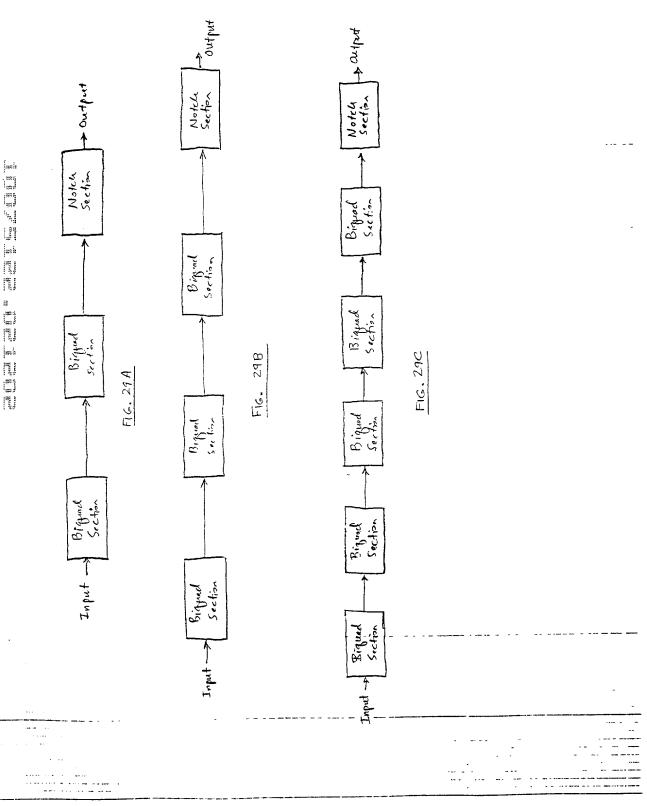


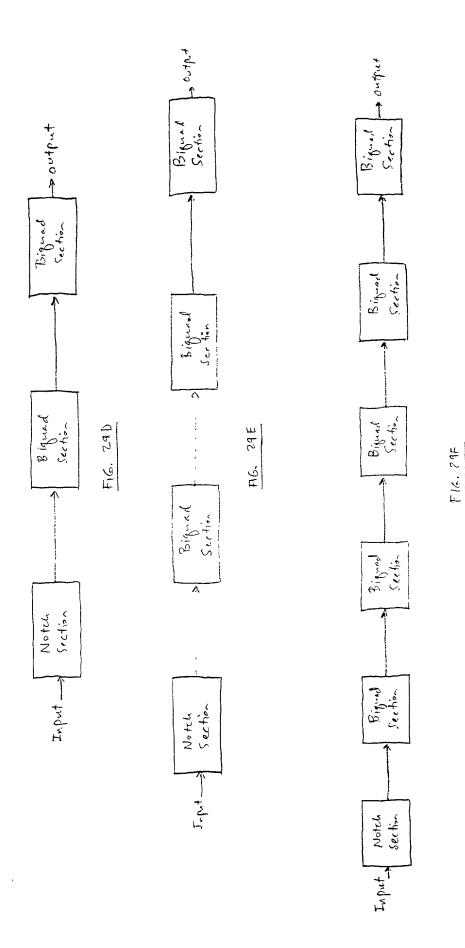
FIG. 27

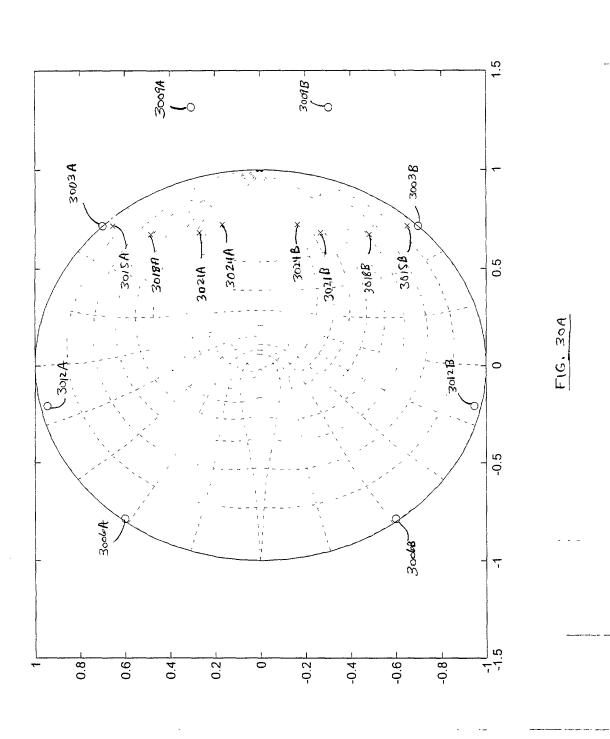


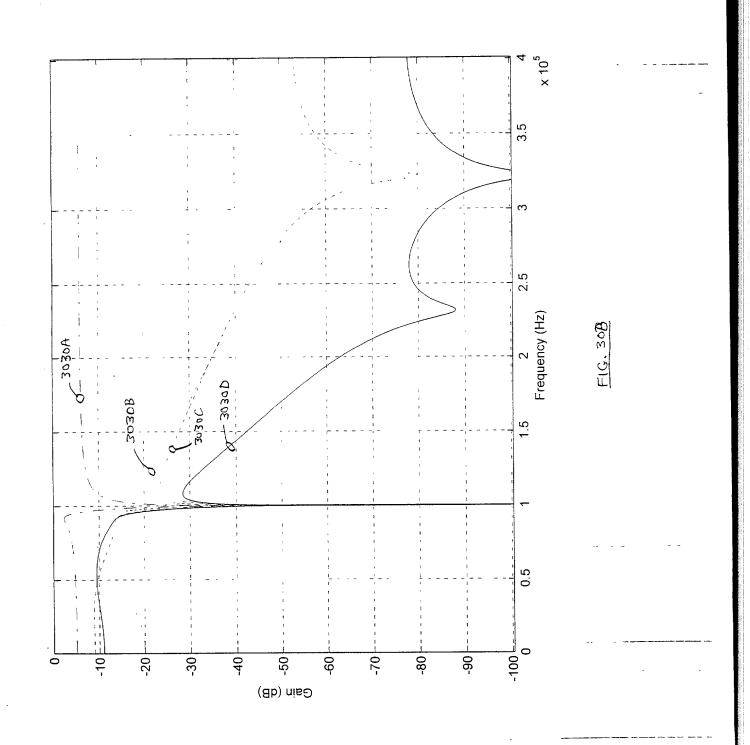
F16.28A

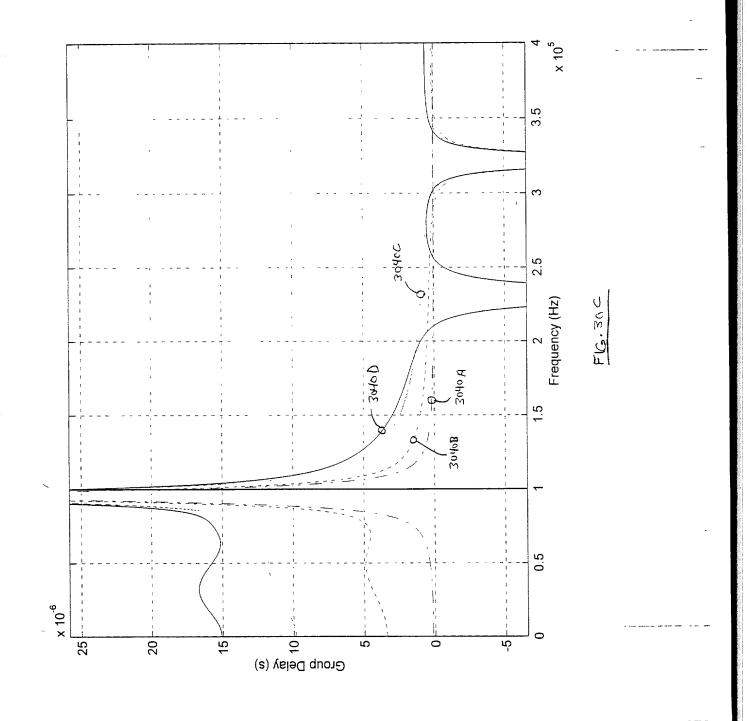


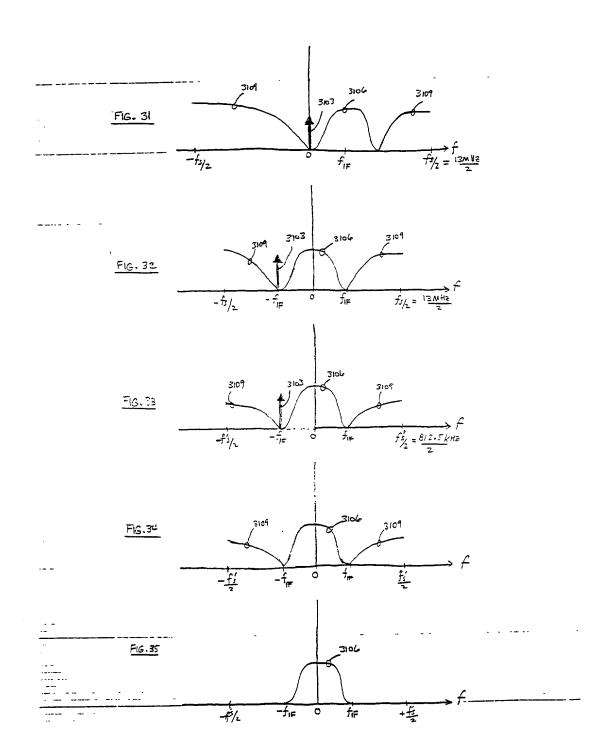












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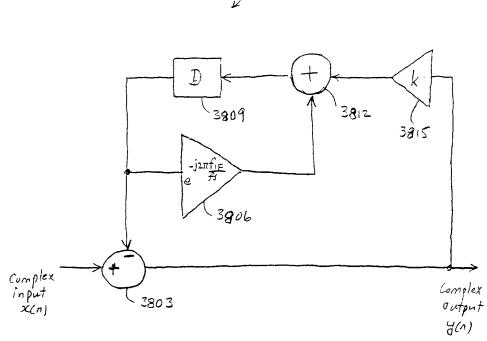


FIG. 38



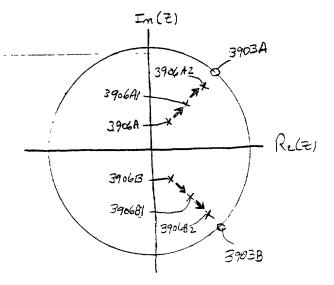


FIG. 39

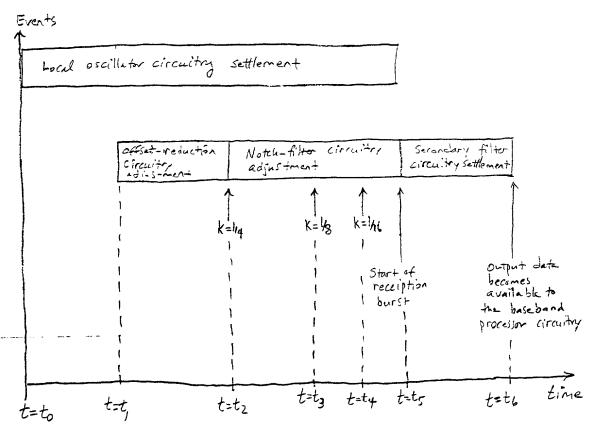


Fig. 40